

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Tadayoshi Nakano et al.

Application No.: 10/807,450

Filed: March 24, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT IN WHICH VOLTAGE DOWN CONVERTER OUTPUT CAN BE OBSERVED AS DIGITAL

VALUE AND VOLTAGE DOWN CONVERTER OUTPUT VOLTAGE

MAIL STOP AF

Group Art Unit: 2819

Examiner: LINH V. NGUYEN

Confirmation No.: 3365

AMENDMENT UNDER 37 C.F.R. §1.116

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

IS ADJUSTABLE

Date: May 12, 2006

Sir:

This Amendment responds to the Office Action dated February 13, 2006

OK to Enter 5/19/06

(Paper No. 20060124). Please amend the above-noted application as follows:

05/15/2006 SZEWDIE1 00000058 10807450

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